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Washington, D.C. 20231

By: Maisie C. Livengood  
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# 3

J1046 U.S. PTO  
10/099754  
03/14/02

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Michael J. Peters et al.

Application No.: TO BE ASSIGNED

Filed: HEREWITH

For: INTEGRATED CIRCUIT I/O  
PAD CELL MODELING

Examiner: Unassigned

Art Unit: Unassigned

INFORMATION DISCLOSURE  
STATEMENT UNDER 37 CFR §1.97 and  
§1.98

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

The references cited on attached form PTO/SB/08A and PTO/SB/08B are being called to the attention of the Examiner. In accordance with 37 CFR §1.98(d), copies of the references can be found in Application No. 08/2199,395, filed September 1, 1994 (Attorney Docket No. 00939S-050500US). It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

Applicant believes that no fee is required for submission of this statement, since it is being submitted prior to the first Office Action. However, if a fee is required, the

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Attorney Docket No.: 00939S-050510US  
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PATENT

Commissioner is authorized to deduct such fee from the undersigned's Deposit Account No. 20-1430. Please deduct any additional fees from, or credit any overpayment to, the above-noted Deposit Account.

Respectfully submitted,

  
Chad E. King  
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DE 7064361 v1

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PTO/SB/08A (08-00)

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Substitute for form 1449A/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 2

Complete if Known

Application Number	TO BE ASSIGNED
Filing Date	HEREWITH
First Named Inventor	Peters, Michael J.
Group Art Unit	
Examiner Name	

Attorney Docket Number 00939s-050510US

10/09/54  
10/09/54  
03/14/02

### U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
	AA	4,821,173		Young, et al.	4/1989	
	AB	5,166,937		Blecha, Jr.	11/1992	
	AC	5,436,591		Ginetti et al.	6/1995	
	AD	5,572,437		Rostoker et al.	11/1996	

### FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
	AE	JP	4273382			2/1993		

Examiner Signature

Date Considered

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number. <sup>2</sup> See attached Kinds of U.S. Patent Documents. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449B/PTO				<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>				Application Number	
Sheet	2	of	2	Filing Date	
				First Named Inventor	Peters, Michael J.
				Group Art Unit	
				Examiner Name	
				Attorney Docket Number	00939s-050510US

<b>OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS</b>			
Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	AF	IBM Technical Disclosure Bulletin; Vol. 30, No. 4; September 1987; Testing Multiple Discrete Software Components by Connecting Real and Simulated Hardware Components; pp 1844,455	
	AG	An Emulator for CMOS ASICS; VLSI Systems Design; May 1987; pp. 8	
	AH	1990 IEEE International Symposium on Circuits and Systems, May 1,1990; P. Agrawal et al; "Can Logic Simulators Handle Bidirectionality and Change Sharing?"; pp. 411-414	
	AI	IEEE Design & Test of Computers; vol. 7, no. 3, June 1990; Coelho; "A VHDL Standard Package for Logic Modeling"; pp. 25-32	

Examiner Signature	Date Considered
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